What is claimed is:

1. In a universal serial bus (USB) interface, an improvement for EMC immunity comprising:

a. a controller including first and second data lines, a power line, and a ground line;

b. a choke comprising a ferrite core through which said first and second data lines and said ground line pass; and

c. wherein said power line is AC coupled to said ground line outside said choke.

2. The improvement of claim 1, wherein said controller further includes a plurality of communication pins.

3. The improvement of claim 1, wherein each of said plurality of communication pins is terminated.

4. The improvement of claim 1, further compromising a power source having a bypass capacitor and a transorb device.

5. The improvement of claim 1, wherein said first and second lines are terminated by capacitors.

6. The improvement of claim 1, wherein said power line is routed through high impedance.

7. The improvement of claim 1, said controller further including a reset input and a strobe output, wherein said power line, said reset input, and said strobe output are filtered by RC networks to prevent transients from inducing modified signal states.

8. In a universal bus (USB) interface including a serial interface controller, an improvement for EMC immunity comprising:

a. first, second, third, and fourth signal lines interconnected to said controller; and

b. a choke comprising a ferrite core through which each of said first, second, third, and fourth signal lines pass.

9. The improvement of claim 8, wherein said IC further includes a plurality of communication pins.

10. The improvement of claim 9, wherein each of said plurality of communication pins is terminated.

11. The improvement of claim 8, further comprising a power source having a bypass capacitor and a transorb device.

12. The improvement of claim 8, wherein said first and second lines are terminated by capacitors.